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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO	
09/893,985	06/29/2001	Jung-Taeck Eu	49128-5017	5660	
9629	7590 07/15/2003				
MORGAN LEWIS & BOCKIUS LLP			EXAMINER		
	SYLVANIA AVENUE NW TON, DC 20004	,	NGUYEN, JI	NGUYEN, JENNIFER T	
			ART UNIT	PAPER NUMBER	
			2674	2	
			DATE MAILED: 07/15/2003		

Please find below and/or attached an Office communication concerning this application or proceeding.



		Application No.	Applicant(s)			
Office Action Summary		09/893,985	EU, JUNG-TAECK			
		Examiner	Art Unit			
		Jennifer T Nguyen	2674			
The MAILING DATE of this communication appears on the cover sheet with the correspondence address Period for Reply						
A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION. - Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication. - If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely. - If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication. - Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). - Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b). Status						
1)🖂	Responsive to communication(s) filed on 29.	<u>June 2001</u> .				
2a) <u></u> ☐	This action is FINAL . 2b)⊠ Th	is action is non-final.				
3)	Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under <i>Ex parte Quayle</i> , 1935 C.D. 11, 453 O.G. 213.					
Disposition of Claims						
4)⊠	Claim(s) $1-7$ is/are pending in the application.					
	4a) Of the above claim(s) is/are withdra	wn from consideration.				
5)□	Claim(s) is/are allowed.					
6)⊠	Claim(s) <u>1-7</u> is/are rejected.					
7)	Claim(s) is/are objected to.	Claim(s) is/are objected to.				
8) Claim(s) are subject to restriction and/or election requirement.						
Application Papers						
9) The specification is objected to by the Examiner.						
10) The drawing(s) filed on is/are: a) accepted or b) objected to by the Examiner. Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CER 1.85(a)						
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a). 11) The proposed drawing correction filed on is: a) approved b) disapproved by the Examiner.						
If approved, corrected drawings are required in reply to this Office action.						
12) The oath or declaration is objected to by the Examiner.						
Priority under 35 U.S.C. §§ 119 and 120						
13)⊠ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).						
a)⊠ All b)□ Some * c)□ None of:						
	1.⊠ Certified copies of the priority documents have been received.					
	2. Certified copies of the priority documents have been received in Application No					
 3. Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)). * See the attached detailed Office action for a list of the certified copies not received. 						
14) Acknowledgment is made of a claim for domestic priority under 35 U.S.C. § 119(e) (to a provisional application).						
a) ☐ The translation of the foreign language provisional application has been received. 15)☐ Acknowledgment is made of a claim for domestic priority under 35 U.S.C. §§ 120 and/or 121.						
Attachment(s)						
1) Notice of References Cited (PTO-892) 2) Notice of Draftsperson's Patent Drawing Review (PTO-948) 3) Information Disclosure Statement(s) (PTO-1449) Paper No(s) 4) Interview Summary (PTO-413) Paper No(s) 5) Notice of Informal Patent Application (PTO-152) 6) Other:						
J.S. Patent and T	rademark Office					

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DETAILED ACTION

Claim Rejections - 35 USC § 102

1. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

- (e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.
- 2. Claims 1-5 and 7 are rejected under 35 U.S.C. 102(e) as being anticipated by Sakaedani et al. (U.S. Patent No. 6,064,360).

Regarding claim 1, referring to Figs. 1 and 4, Sakaedani teaches a discharging apparatus for a liquid crystal display including a plurality of liquid crystal pixel cells (FET), each of the liquid crystal cells (FET) being arranged at an intersection between one of a plurality of gate lines (32) and one of a plurality of data lines, and switching devices (31) for driving the liquid crystal cells in response to a signal from each gate line (32), the discharging apparatus (35) comprising: a first gate voltage supply line (Vgh); a second gate voltage supply line (Vgl); a power supply line (Vdd); gate driver circuitry (33) for selectively applying to the gate lines first and second gate voltages supplied from the first (Vgh) and second (Vgl) gate voltage supply lines, respectively; and a discharge circuit (35) for sensing a power-off condition of the power supply line (Vdd) to short circuit the first gate voltage supply line (Vgh) and the second gate voltage supply line (Vgl) when the power-off condition is sensed, thereby discharging voltages on the gate lines (32) (col. 3, lines 57-67, col. 4, lines 1-67, and col. 5, lines 1-15).

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Regarding claim 2, Sakaedani teaches the first gate voltage (Vgh) is a positive gate high voltage, and the second gate voltage (Vgl) is a gate low voltage, and the gate low voltage is negative relative the positive gate high voltage (col. 4, lines 1-5).

Regarding claim 3, Sakaedani teaches the discharge circuit includes: power-off sensing circuitry for sensing the power-off condition of the power supply line (Vdd); and a switching device (37) for short-circuiting the first and second gate voltage supply lines upon power-off in response to a control signal from the power-off sensing circuitry (col. 4, lines 6-67 and col. 4, lines 1-15).

Regarding claim 4, Sakaedani teaches the discharge circuit (35) includes: a capacitor (C2) for charging to a desired voltage when a power voltage is being applied from the power supply line and discharging the charged desired voltage upon the power-off condition; and a switching control device (40) for controlling the switching device in response to the desired voltage discharged from the capacitor (col. 4, lines 34-67 and col. 5, lines 16).

Regarding claim 5, Sakaedani teaches the desired voltage charged in the capacitor (C2) comprises a voltage difference between the gate high voltage (Vgh) and the power voltage (Vdd), and the capacitor discharges the desired voltage upon the power-off condition (col. 4, lines 34-67 and col. 5, lines 16).

Regarding claim 7, Sakaedani teaches the discharge circuit (35) is provided on a printed circuit board and connected to the gate driver circuitry (col. 5, lines 26-33).

Claim Rejections - 35 USC § 103

3. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

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(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.

4. Claim 6 is rejected under 35 U.S.C. 103(a) as being unpatentable over Sakaedani et al. (U.S. Patent No. 6,064,360) in view of Moon et al. (U.S. Patent No. 5,945,970).

Regarding claim 6, Sakaedani teaches all the limitations of the claimed invention and further teaches wherein the switching device (37) is a NPN-type transistor and the switching control device (40) is a NPN-type transistor (col. 5, lines 16-37).

Sakaedani fails to teach the switching control device transistor is a PNP-type. However, referring to Fig. 9, Moon teaches a switching control device transistor is a PNP-type (T4) (col. 7, lines 18-60). Therefore, it would have been obvious to one of ordinary skill in the art at the time the invention was made to incorporate the switching control device transistor is a PNP-type as taught by Moon in the system of Sakaedani in order to provide a better charge carrier to the switching device in response to the desired voltage discharged from the capacitor.

5. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure:

Tanikawa (U.S. Patent No. 5,572,735) teaches method and apparatus for discharging the output voltage of a DC power supply.

Moon (U.S. Patent No. 5,793,346) teaches LCD device having active screen clearing circuits therein.

Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to **Jennifer T. Nguyen** whose telephone number is **703-305-3225**. The examiner can normally be reached on Mon-Fri from 9:00-5:30.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, **Richard A Hjerpe** can be reach at **703-305-4709**.

Any response to this action should be mailed to:

Commissioner of Patents and Trademarks

Washington, DC. 20231

Or faxed to: 703-872-9314 (for Technology Center 2600 only)

Hand-delivered responses should be brought to Crystal Park II, 2121 Crystal Drive, Arlington, VA, sixth-floor (Receptionist).

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the Technology Center 2600 Customer Service Office whose telephone number is 703-306-0377.

Jennifer T. Nguyen Patent examiner Art Unit 2674

SUPERVISORY PATENT EXAMINER

TECHNOLOGY CENTER 2800